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| FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b)) | | | | Attorney Docket No.: P2001,0099 Appl. No. Applicant GUNNAR KRAUSE ET AL. Filing Date February 14, 2002 Group Art Unit | | | |
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| EXAMINER INITIALS | PATENT NO. | DATE | PATENTEE | CLASS | SUB CLASS | FILING DATE |
|----------------------|------------|-----------|----------|--------|--------------|----------------|
| <i>cu</i> | A | 5,971,923 | 10/26/99 | Finger | | |
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| FOREIGN PATENT DOCUMENT | | | | | | | |
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| | DOCUMENT NO. | DATE | COUNTRY | CLASS | SUB CLASS | TRANSL. YES NO | |
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| | M | | | | | | |
| | N | | | | | | |

| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) | |
|--|---|
| <i>cu</i> | Ikeda, H.: "High-Speed DRAM Architecture Development", IEEE, Vol. 34, No. 5, May 1999, pp. 685-692 |
| <i>cu</i> | "DDR SDRAM Functionality and Controller Read Data Capture", Micron Technology Inc., Vol. 8, Issue 3, pp. 1-24 |

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| EXAMINER <i>Wang</i> | DATE CONSIDERED 1-26-05 |
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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